

APPENDIX A
“CLEAN” VERSION OF EACH PARAGRAPH/SECTION/CLAIM
37 C.F.R. § 1.121(b)(ii) AND (c)(i)

CLAIMS (with indication of amended or new):

Please amend claim 1 as follows:

1. (Amended) A current sense integrated circuit, comprising:
 - an amplifier circuit for receiving and amplifying a differential analog input signal at a first voltage level containing current sense information, wherein the amplifier circuit includes a circuit to minimize inherent temperature offset drift;
 - a pulse width modulator circuit for converting the differential analog input signal to a pulse width modulated signal at the first voltage level; and
 - a level shift circuit for converting the pulse width modulated signal from the first voltage level to a second voltage level.